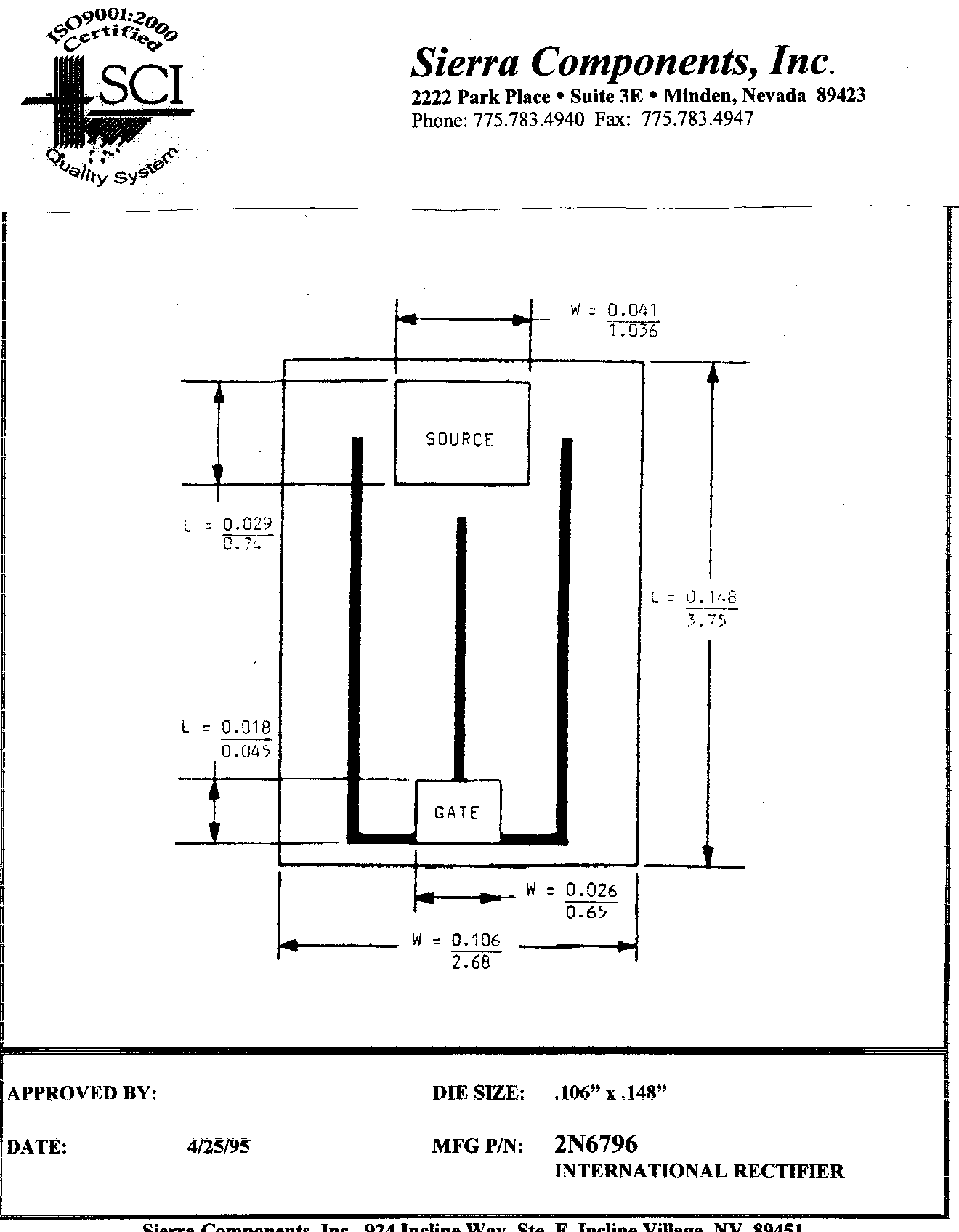
Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.106”**



**.148”**

**Top Material: Al**

**Backside Material: Si**

**S = .030 x .041”**

**G = .018 x .025”**

**Backside Potential: DRAIN**

**APPROVED BY: DK DIE SIZE .106” X .148” DATE: 12/15/22**

**MFG: INFINEON THICKNESS .000” P/N: 2N6796**

**DG 10.1.2**

#### Rev B, 7/1